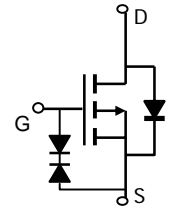




MT3415 P-Channel 20V(D-S) MOSFET

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-20V	50mΩ@-4.5V	-4.0A
	60mΩ@-2.5V	
	100mΩ@-1.8V	



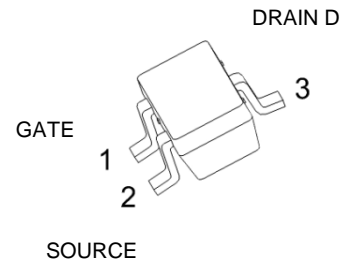
FEATURE

Excellent $R_{DS(ON)}$, low gate charge, low gate voltages

APPLICATIONS

Load switch and in PWM applicatopns

MARKING: R15



SOT-23-3L

Maximum ratings ($T_a=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	±8	
Continuous Drain Current ($t \leq 10s$)	I_D	-4.0	A
Maximum Power Dissipation ($t \leq 10s$)	P_D	0.30	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	417	$^{\circ}C/W$
Operation Junction and Storage Temperature Range	T_J, T_{STG}	-55 ~ +150	$^{\circ}C$



MOSFET ELECTRICAL CHARACTERISTICS

$T_a=25\text{ }^\circ\text{C}$ unless otherwise specified

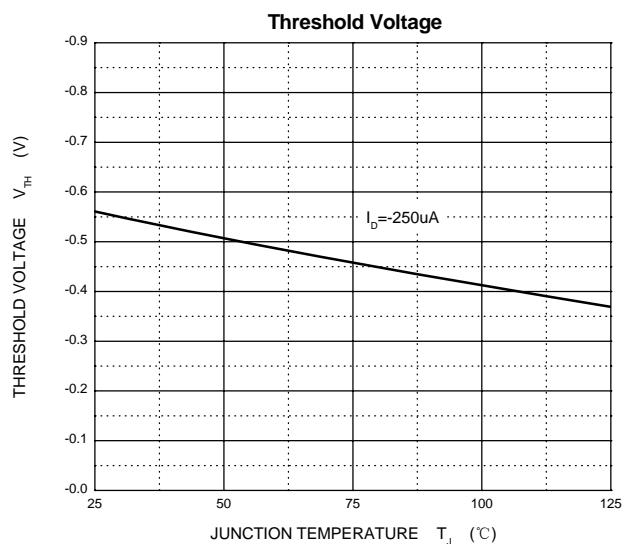
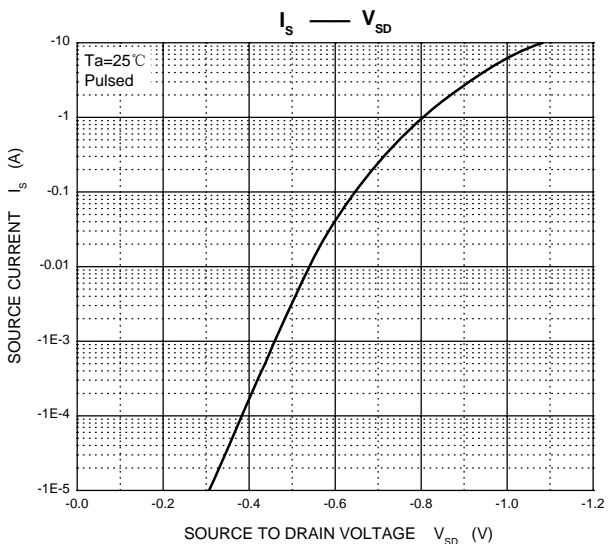
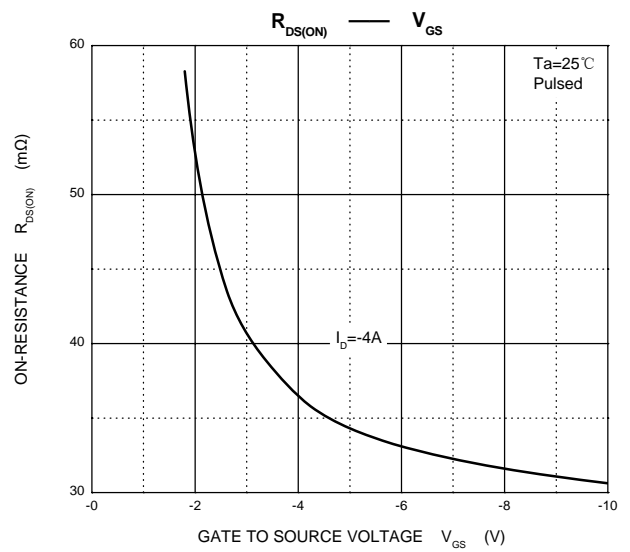
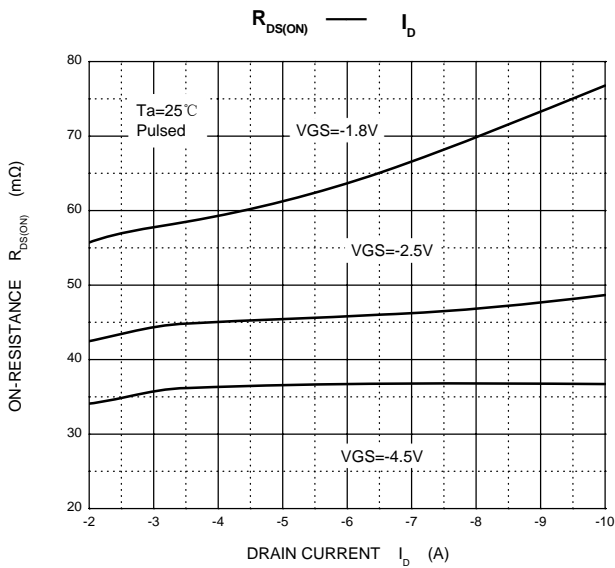
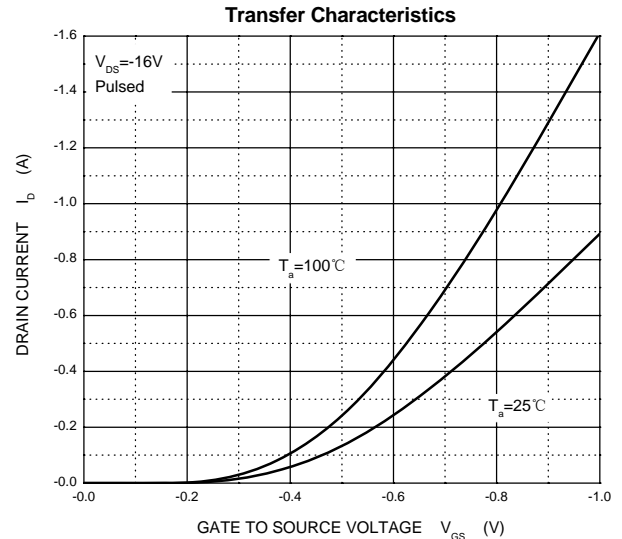
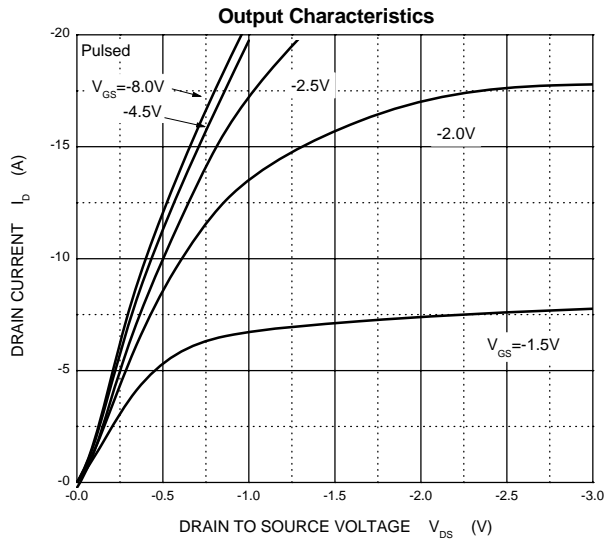
Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static Parameters						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-20			V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.3	-0.56	-1	
Gate-body leakage current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 8V$			± 10	μA
		$V_{DS} = 0V, V_{GS} = \pm 4.5V$			± 1	
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -16V, V_{GS} = 0V$			-1	
Drain-source on-state resistance(note2)	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -4A$		0.037	0.050	Ω
		$V_{GS} = -2.5V, I_D = -4A$		0.045	0.060	
		$V_{GS} = -1.8V, I_D = -2A$		0.080	0.100	
Forward transconductance(note2)	g_{FS}	$V_{DS} = -5V, I_D = -4A$	8			S
Body diode voltage(note2)	V_{SD}	$I_S = -1A, V_{GS} = 0V$			-1	V
Dynamic Parameters (note3)						
Input capacitance	C_{iss}	$V_{DS} = -10V, V_{GS} = 0V, f = 1MHz$		1450		μF
Output capacitance	C_{oss}			205		
Reverse transfer capacitance	C_{rss}			160		
Gate resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		6.5		Ω
Switching Parameters						
Total gate charge	Q_g	$V_{DS} = -10V, V_{GS} = -4.5V, I_D = -4A$		17.2		nC
Gate-Source charge	Q_{gs}			1.3		
Gate-drain charge	Q_{gd}			4.5		
Turn-on delay time (note3)	$t_{d(on)}$	$V_{DS} = -10V, V_{GS} = -4.5V$ $R_{GEN} = 3\Omega, R_L = 2.5\Omega,$		9.5		ns
Turn-on rise time(note3)	t_r			17		
Turn-off delay time(note3)	$t_{d(off)}$			94		
Turn-off fall time(note3)	t_f			35		

Notes:

1. Repetitive rating, pulse width limited by junction temperature.
2. Pulse Test : Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. These parameters have no way to verify.

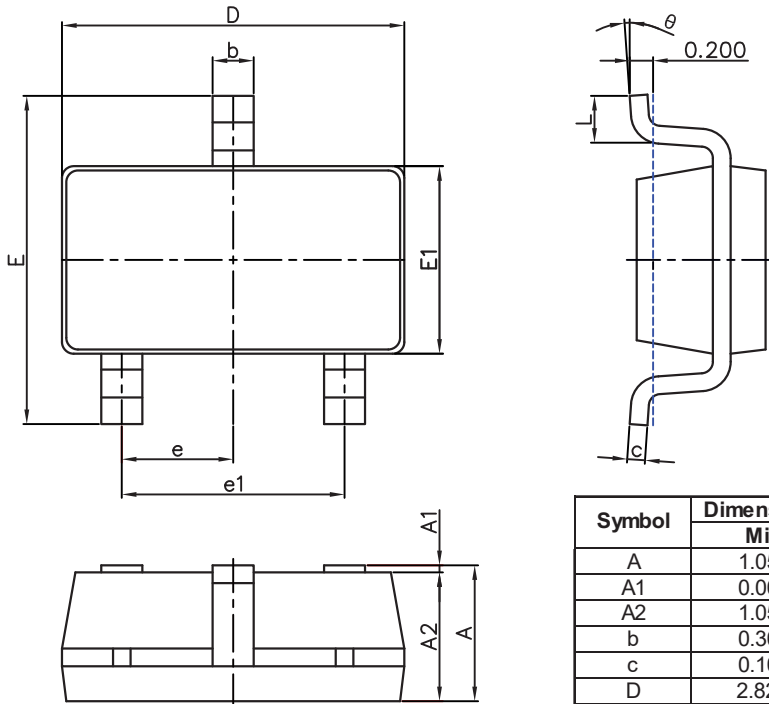


Typical Characteristics



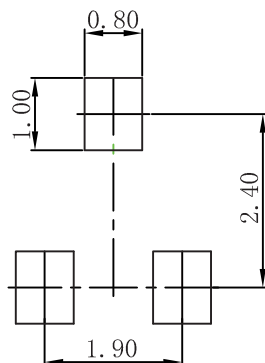


SOT23-3L Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
K	0°	8°	0°	8°

SOT23-3L Suggested Pad Lay out



Note:
 1. Controlling dimension: in millimeters.
 2. General tolerance: $\pm 0.05\text{mm}$.
 3. The pad layout is for reference purposes only.